

Notice of Allowability

Application No.

09/779,498

Examiner

Igor N. Borissov

Applicant(s)

YOSHIDA, HIROSHI

Art Unit

3628

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Interview Summary of January 4, 2007.
2. ☒ The allowed claim(s) is/are 19-23.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

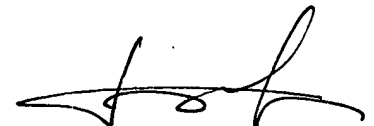
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|--|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input checked="" type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____ |



IGOR N. BORISSOV
PRIMARY EXAMINER

Response to Amendment

Amendment received on 10/24/2006 is acknowledged and entered. Claims 1-18 have been canceled. Claims 19 and 22 have been amended. Claims 19-23 are currently pending in the application.

Claim Rejections under 35 USC § 112 and 35 USC § 103 have been withdrawn due to the applicant's amendment.

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a personal interview with an applicant's representative Deidre M. Davis (Reg. No.: 52,797) on Thursday, January 4, 2007.

The application has been amended as follows:

IN THE CLAIMS

1. (cancelled)
2. (cancelled)
3. (cancelled)
4. (cancelled)

Art Unit: 3628

5. (cancelled)

6. (cancelled)

7. (cancelled)

8. (cancelled)

9. (cancelled)

10. (cancelled)

11. (cancelled)

12. (cancelled)

13. (cancelled)

14. (cancelled)

15. (cancelled)

16. (cancelled)

17. (cancelled)

18. (cancelled)

19. (currently amended) A server apparatus for controlling the transit of information relative to a noise countermeasure, comprising:

a noise countermeasure database comprising:

Art Unit: 3628

a circuit information check item table used to check whether circuit elements value transmitted from a user terminal exceeds a predetermined value, and to record a result thereof; and

a circuit requisite information table including recorded circuit requisite information which is required to prevent the circuit elements value from exceeding the predetermined value;

a processor configured to:

~~a circuit information acquiring unit to acquire circuit information from [[a]] the user terminal connected via a network, the circuit information being included in items corresponding to a state of electronic circuits[[:]], wherein the said items include including at least one information of circuit elements values [[or]] and mounted component positions[[,]] regarding printed-circuit boards and Large Scale Integration (LSI) circuits;~~

~~a registered noise countermeasure information storing unit to store noise countermeasure information in [[a]] the noise countermeasure database, the noise countermeasure information is requested for registration by a registration terminal connected to the server apparatus via the network; wherein the noise countermeasure database comprises~~

~~a circuit information check item table and a circuit requisite information table, wherein the circuit information check item table is a table used to check whether the circuit elements value transmitted from the user terminal exceeds a predetermined value and to record a result thereof; wherein the circuit requisite information table is recorded circuit requisite information which is required to prevent the circuit elements value from exceeding the predetermined value;~~

~~a noise countermeasure list information generating unit access said recorded circuit requisite information from said circuit requisite information table and [[to]] generate noise countermeasure list information based on said registered noise countermeasure information and said recorded circuit requisite information, the generated noise countermeasure list information including a plurality of noise countermeasure processes and transmitting the generated noise countermeasure list information to said user terminal; and~~

~~a noise countermeasure information determining unit to execute one of the noise countermeasure processes selected by the user from said noise countermeasure list information, according to the said items, which is required for the noise countermeasure[[,]]; and~~

[[to]] transmit noise countermeasure information which is determined as a result of the execution of the one of the plurality of noise countermeasure processes, to said user terminal.

20. (currently amended) The server apparatus of claim 19, further comprising a charging control unit means for [[to]] performing a charging control process to charge a user for usage of a registered noise countermeasure.

21. (currently amended) The server apparatus of claim 20, wherein said noise countermeasure database further comprises usage point information for a group of users which can use the registered noise countermeasure information, and

~~wherein the charging control unit comprises a usage point for a group that uses the registered noise countermeasure information to charge for usage of the registered noise countermeasure, to thereby~~ means is further configured to: access the usage point information from the database; charge said group of users for usage of the registered noise countermeasure; add a usage point each time the registered noise countermeasure is used to update said usage point information[[,.]]; and [[to]] manage an amount of money to be paid to a registrant.

22. (currently amended) A system for controlling the transit of information relative to a noise countermeasure, comprising:

a server apparatus comprising:

a noise countermeasure database comprising:

a circuit information check item table used to check whether circuit elements value transmitted from a user terminal exceeds a predetermined value, and to record a result thereof; and

a circuit requisite information table including recorded circuit requisite information which is required to prevent the circuit elements value from exceeding the predetermined value;

a processor configured to:

Art Unit: 3628

~~a circuit information acquiring unit to acquire circuit information from [[a]] the user terminal connected via a network, the circuit information being included in items corresponding to a state of electronic circuits[[]], wherein the said items include including at least one information of circuit element values or mounted component positions[[]] regarding printed-circuit boards and Large Scale Integration (LSI) circuits;~~
~~a registered noise countermeasure information storing unit to store noise countermeasure information in [[a]] the noise countermeasure database, the noise countermeasure information is requested for registration by a registration terminal connected to the server apparatus via the network;~~

~~wherein the noise countermeasure database comprising, a circuit information check item table and a circuit requisite information table, — wherein the circuit information check item table is a table used to check whether the circuit elements value transmitted from the user terminal exceeds a predetermined value and to record a result thereof; — wherein the circuit requisite information table is recorded circuit requisite information which is required to prevent the circuit elements value from exceeding the predetermined value;—,~~

~~a noise countermeasure list information generating unit access said recorded circuit requisite information from said circuit requisite information table and [[to]] generate noise countermeasure list information based on said registered noise countermeasure information and said recorded circuit requisite information, the generated noise countermeasure list information including a plurality of noise countermeasure processes and transmitting the generated noise countermeasure list information to said user terminal;~~

~~a noise countermeasure information determining unit to execute one of the noise countermeasure processes selected by the user from said noise countermeasure list information, according to the said items, which is required for the noise countermeasure[[]]; and [[to]] transmit noise countermeasure information which is determined as a result of the execution of the one of the plurality of noise countermeasure processes, to said user terminal;~~

~~a charging control unit to means for performing a charging control process which respect to said determined noise countermeasure information provided; and~~

~~a client apparatus comprising the registration terminal and the user terminal, connected to said server apparatus via the network, each including at least one of:~~

Art Unit: 3628

an information registration requesting ~~unit to~~ means for requesting said server apparatus to register noise countermeasure information, and

an information usage processing ~~unit to~~ means for transmitting circuit information to said server apparatus, ~~[[to]]~~ performing a user interface control process on noise countermeasure list information transmitted from said server apparatus, ~~to receive~~ receiving noise countermeasure information transmitted from said server apparatus and ~~[[to]]~~ transmitting an identifier to the client apparatus.

23. (currently amended) The system of claim 22, wherein said noise countermeasure database further comprises usage point information for a group of users which can use the registered noise countermeasure information, and

wherein ~~the said~~ charging control ~~unit means~~ is further configured to: set~~[[s]]~~ a usage point for ~~[[a]]~~ said group of users that uses the registered noise countermeasure information; access the usage point information from the database; ~~[[to]]~~ charge said group of users for usage of a registered noise countermeasure, add~~[[s]]~~ a usage point each time the registered noise countermeasure is used to update said usage point information~~[[.]]~~; and manages an amount of money to be paid to a registrant.

Allowable Subject Matter

Claims 19-23 are allowed.

The following is an examiner's statement of reasons for allowance:

As per independent claim 19, the best prior art, Tsuchida et al. (US 5,559,997) in view of Takahiro et al. (JP 08297689 A) teaches a computer apparatus comprising: means for storing noise countermeasure information requested for registration by a registration terminal in the registration terminal connected via a network; means for acquiring circuit information from a user terminal connected via the network, which uses

Art Unit: 3628

the registered noise countermeasure information and circuit information included in items corresponding to a state of electronic circuits; means for generating noise countermeasure information based on said registered noise countermeasure information and said circuit information, said generated noise countermeasure information including a plurality of noise countermeasure processes (components and processes of installing said components); means for executing one of the noise countermeasure processes selected by the user from said noise countermeasure information.

However, Tsuchida et al. in view of Takahiro et al. fails to teach that said noise countermeasure database comprises a circuit information check item table used to check whether circuit elements value transmitted from a user terminal exceeds a predetermined value, and to record a result thereof; and a circuit requisite information table including recorded circuit requisite information which is required to prevent the circuit elements value from exceeding the predetermined value; and that said processor is configured to access said recorded circuit requisite information from said circuit requisite information table and generate noise countermeasure list information based on said registered noise countermeasure information and said recorded circuit requisite information.

As per independent claim 22, the best prior art, Tsuchida et al. in view of Takahiro et al. and further in view of Robertson et al. (US 6,594,799) teaches a computer apparatus comprising: means for storing noise countermeasure information requested for registration by a registration terminal in the registration terminal connected via a network; means for acquiring circuit information from a user terminal connected via the network, which uses the registered noise countermeasure information and circuit information included in items corresponding to a state of electronic circuits; means for generating noise countermeasure information based on said registered noise countermeasure information and said circuit information, said generated noise countermeasure information including a plurality of noise countermeasure processes (components and processes of installing said components); means for executing one of the

Art Unit: 3628

noise countermeasure processes selected by the user from said noise countermeasure information, wherein said system is implemented in a client/server configuration.

However, Tsuchida et al. in view of Takahiro et al. and further in view of Robertson et al. fails to teach that said noise countermeasure database comprises a circuit information check item table used to check whether circuit elements value transmitted from a user terminal exceeds a predetermined value, and to record a result thereof; and a circuit requisite information table including recorded circuit requisite information which is required to prevent the circuit elements value from exceeding the predetermined value; and that said processor is configured to access said recorded circuit requisite information from said circuit requisite information table and generate noise countermeasure list information based on said registered noise countermeasure information and said recorded circuit requisite information.

The best NPL prior art, the Internet printout of <http://esdpcb.com>, while teaching customized circuit design, fails to disclose a database comprising a circuit information check item table used to check whether circuit elements value transmitted from a user terminal exceeds a predetermined value, and to record a result thereof; and a circuit requisite information table including recorded circuit requisite information which is required to prevent the circuit elements value from exceeding the predetermined value; and a processor configured to access said recorded circuit requisite information from said circuit requisite information table and generate noise countermeasure list information based on said registered noise countermeasure information and said recorded circuit requisite information.

The remaining dependent claims are considered allowable, as they are dependent and based off of an allowable independent claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

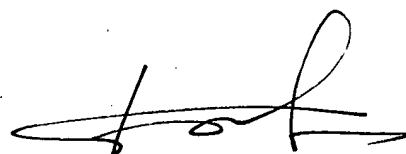
Art Unit: 3628

accompany the issue fee. Such submission should be clearly labeled "Comments on Statement of Reason for Allowance".

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Igor Borissov whose telephone number is 703-305-4649. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Weiss can be reached on 703-308-2702. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



IGOR N. BORISSOV
PRIMARY EXAMINER

IB
01/05/2007